

**AMENDMENTS TO THE CLAIMS**

**Please amend the claims as follows:**

141. (Amended) An apparatus for processing digital data, the apparatus comprising:

a processor; and

a memory connected to the processor and including executable instructions that when executed by the processor, cause the processor to perform:

receiving digital data including a plurality of transport packets having a header and a scrambled data unit;

detecting a header of a first transport packet included in the plurality of transport packets;

extracting a marker from the header of the first transport packet;

extracting first control data from the extracted marker;

descrambling, using the same first control data and the same descrambler, both scrambled digital video and audio data included in the scrambled data unit of the first transport packet and one or more succeeding scrambled data units in the transport packets following the first transport packet;  
and

determining a number of transport packets that have been processed, and

when the processor determines the number of transport packets is a minimum of a multiple of four transport packets, the executable instructions further cause the processor to perform

detecting the header of a next transport packet included in the plurality of transport packets;

extracting a marker from the header of the next transport packet;

extracting second control data from the extracted marker; and

descrambling, using the same second control data and the same descrambler, both scrambled digital video and audio data included in the scrambled data unit of the next transport packet and one or more succeeding scrambled data units in the transport packets following the next transport packet.